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**Examiner:** Tran Andrew Q. **Group:** 2824

**Date:** October 27, 2005

**Client Code:** 2037

**Facsimile No.:** 571-273-8300

**From:** James M. Smith, Esq.

**Subject: Paper:** Reply to Restriction Requirement

**Docket No.:** 2037.1005-002

**Applicant:** Peter B. Gillingham

**Serial No.:** 09/654,367

**Filing Date:** September 1, 2000

Number of pages including this cover sheet: 2

Please confirm receipt of facsimile: Yes XX No     

**Comments:**

@PFDskupl:ODMA/MHODMA/HBSRU5;Manage.583563.1

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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OCT 27 2005

Applicant: Peter B. Gillingham

Application No.: 09/654,367

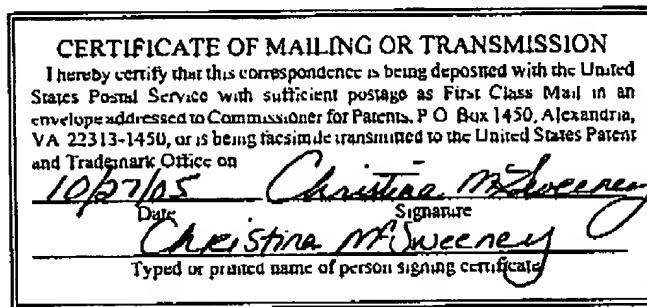
Group: 2824

Filed: September 1, 2000

Examiner: Tran Andrew Q.

Confirmation No.: 8231

For: METHOD OF MULTI-LEVEL STORAGE IN DRAM AND  
APPARATUS THEREOF



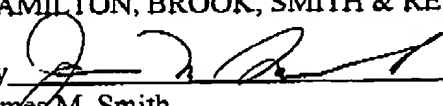
REPLY TO RESTRICTION REQUIREMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Restriction Requirement dated September 27, 2005, the claims of Group 1 (Claims 10-14) drawn to a method for storing a multi-bit value, classification in class 365, subclass 168 are elected for prosecution. Applicant reserves the right to file a continuing application or take such other appropriate action as deemed necessary to protect the non-elected inventions. Applicant does not hereby abandon or waive any rights in the non-elected inventions.

Respectfully submitted,  
HAMILTON, BROOK, SMITH & REYNOLDS, P.C.

By   
James M. Smith  
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Dated: 10/27/5